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I. AMENDMENT

In the claims:

1. (previously presented) A communication circuit, comprising:

a network interface operable to receive a data packet having a header; and

a processor coupled to the network interface and operable

to perform a checksum operation on the received data packet, and if the checksum operation indicates that the data packet contains an error, to then change the received data packet to include an error indicator.

2. (original) The circuit of claim 1 wherein the data packet comprises a TCP data packet.

3. (original) The circuit of claim 1 wherein the data packet comprises an IP data packet.

4. (original) The circuit of claim 1 wherein the data packet comprises an UDP data packet.

5. (previously presented) A communication circuit, the circuit comprising:

a network interface operable to receive a data packet having a header;

a processor coupled to the network interface and operable to perform a checksum operation on the received data packet and operable to change the received data packet in response to the checksum operation;

wherein the header comprises a checksum field; and

wherein changing the received data packet comprises changing the checksum field.

6. (original) The circuit of claim 5 wherein:

the checksum field comprises a plurality of bits; and